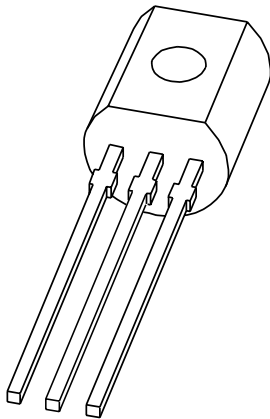


# DATA SHEET



## **ED1402** NPN general purpose transistor

Product specification  
Supersedes data of 1997 Sep 04

1999 Apr 27

# NPN general purpose transistor

ED1402

## FEATURES

- Low current (max. 200 mA)
- Low voltage (max. 20 V)
- High gain.

## APPLICATIONS

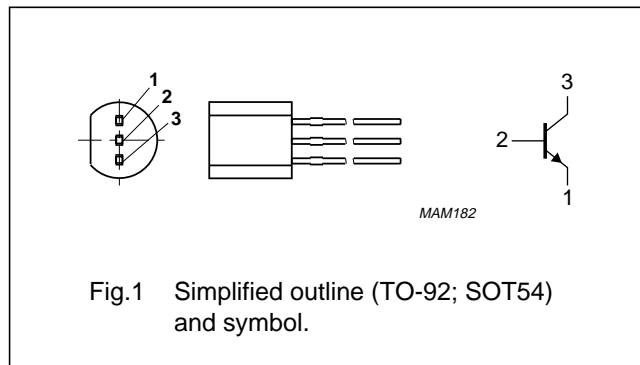
- General purpose switching and amplification.

## DESCRIPTION

NPN transistor in a plastic TO-92; SOT54 package.  
PNP complement: ED1602.

## PINNING

PIN	DESCRIPTION
1	emitter
2	base
3	collector



## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	–	28	V
$V_{CEO}$	collector-emitter voltage	open base	–	20	V
$V_{EBO}$	emitter-base voltage	open collector	–	6	V
$I_C$	collector current (DC)		–	200	mA
$I_{CM}$	peak collector current		–	200	mA
$I_{BM}$	peak base current		–	200	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25\text{ }^\circ\text{C}$ ; note 1	–	500	mW
$T_{stg}$	storage temperature		–65	+150	$^\circ\text{C}$
$T_j$	junction temperature		–	150	$^\circ\text{C}$
$T_{amb}$	operating ambient temperature		–65	+150	$^\circ\text{C}$

## Note

1. Refer to TO-92; SOT54 standard mounting conditions.

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## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	250	K/W

## Note

1. Refer to TO-92; SOT54 standard mounting conditions.

## CHARACTERISTICS

$T_{amb} = 25\text{ °C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT	
$I_{CBO}$	collector cut-off current	$I_E = 0; V_{CB} = 30\text{ V}$	–	15	nA	
		$I_E = 0; V_{CB} = 30\text{ V}; T_j = 150\text{ °C}$	–	5	$\mu\text{A}$	
$I_{EBO}$	emitter cut-off current	$I_C = 0; V_{EB} = 5\text{ V}$	–	100	nA	
$h_{FE}$	DC current gain	$I_C = 2\text{ mA}; V_{CE} = 5\text{ V}$				
			ED1402B	150	225	
			ED1402C	202	318	
			ED1402D	290	450	
	ED1402E	410	810			
$V_{BE}$	base-emitter voltage	$I_C = 2\text{ mA}; V_{CE} = 5\text{ V}; \text{note 1}$	–	770	mV	
$C_c$	collector capacitance	$I_E = I_e = 0; V_{CB} = 10\text{ V}; f = 1\text{ MHz}$	–	5	pF	
$f_T$	transition frequency	$I_C = 1\text{ mA}; V_{CE} = 10\text{ V}; f = 100\text{ MHz}$	100	–	MHz	

## Note

1. Pulse test:  $t_p \leq 300\text{ }\mu\text{s}$ ;  $\delta \leq 0.02$ .

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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b <sub>1</sub>	c	D	d	E	e	e <sub>1</sub>	L	L <sub>1</sub> <sup>(1)</sup>
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOT54		TO-92	SC-43		97-02-28

## NPN general purpose transistor

ED1402

**DEFINITIONS**

<b>Data Sheet Status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

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**NOTES**

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**NOTES**

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